



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Heppler

Serial No.: 08/715,869

Filed: September 19, 1996

For: METHOD AND APPARATUS FOR

TESTING INTEGRATED CIRCUITS

Confirmation No.: 5113

Examiner: J. Rodriguez

Group Art Unit: 3653

Attorney Docket No.: 2269-2972US

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NOTICE OF EXPRESS MAILING

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SUPPLEMENTAL REISSUE DECLARATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir

I hereby declare that:

I am the sole inventor of the claimed invention.

Every error in the patent which was corrected in the present reissue application, and which is not covered by the prior declaration submitted in this application, up to and including the amendment submitted concurrently herewith, arose without any deceptive intention on the part of the Applicant.

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punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

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Date January 24, 2005 BBJ/nj:lh

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